

2023A3-算子实现和性能优化

队伍: arcsysu1

队长: 郭天宇





自我介绍



TIAN-YU GUO 郭天宇

✉ guoty9@mail2.sysu.edu.cn · 🌐 “homepage” · 📄 “gty111”

🎓 EDUCATION

- | | |
|--|------------------------|
| Sun Yat-sen University , Guangzhou, China | 2022 – 2025 (expected) |
| <i>Master student in Computer Science (CS)</i> | |
| Xidian University , Shaanxi, China | 2018 – 2022 |
| <i>B.S. in Computer Science (CS)</i> | |

🧩 EXPERIENCE AND PROJECTS

- | | |
|---|------|
| Optimize GEMM step by step | 2023 |
| “GEMM MMA” first implementates a naive kernel of GEMM by CUDA mma.sync and then optimize it step by step. In the end, it achieves above 60% of peak performance relative to CUTLASS. | |
| Teaching Assistant of “SYSU-DCS3013 : Computer Architecture” | 2022 |
| Release “SYSU-ARCH LAB” which focuses on simulators(gem5, GPGPU-Sim and Accel-Sim). | |
| Design PTX-EMU | 2022 |
| “PTX-EMU” is an emulator for NVIDIA PTX.
You can use it to generate image by simulating rendering program. | |
| Design CNN framework on CPU and GPU | 2022 |
| “CovNN” is a CNN framework support on CPU and GPU.
To validate its availability, CNNs are built to solve MNIST or CIFAR-10 training on GPU and achieve 98% or 70% accuracy respectively. | |

<https://gty111.github.io>

SIMD (Single Instruction, Multiple Data) 通常也被称为“单指令多数据”，是一种较为常见的并行计算技术。它能够同时对多个数据元素执行相同的操作，从而提高程序的执行效率。

Scalar Operation

$$\begin{array}{l} A_1 \times B_1 = C_1 \\ A_2 \times B_2 = C_2 \\ A_3 \times B_3 = C_3 \\ A_4 \times B_4 = C_4 \end{array}$$

SIMD Operation

$$\begin{array}{l} A_1 \\ A_2 \\ A_3 \\ A_4 \end{array} \times \begin{array}{l} B_1 \\ B_2 \\ B_3 \\ B_4 \end{array} = \begin{array}{l} C_1 \\ C_2 \\ C_3 \\ C_4 \end{array}$$

SIMD向量处理器模型

GCU中的SIMD编程

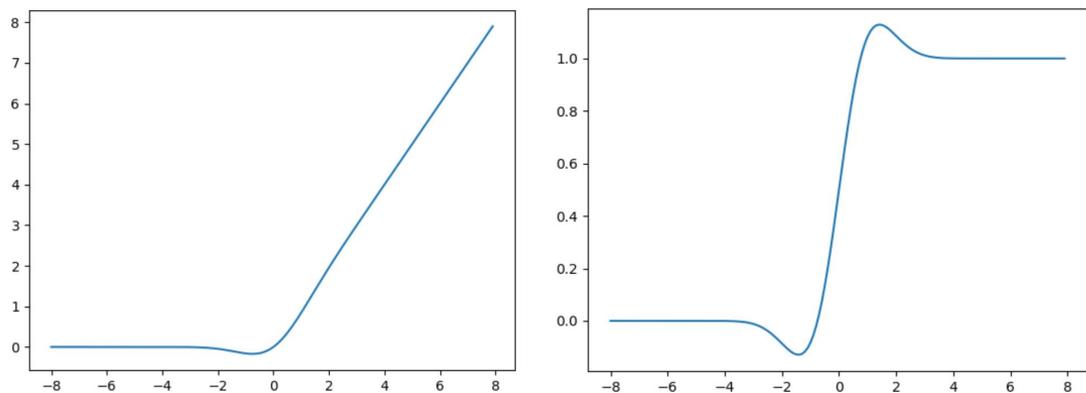
- 数据类型v16f32，代表16个单精度浮点数
- 多线程SIMD访存，竞赛平台采用两线程硬件
- SIMD中的分支分歧与GCU的掩码操作
- SIMD内置函数接口



两线程交替处理数据

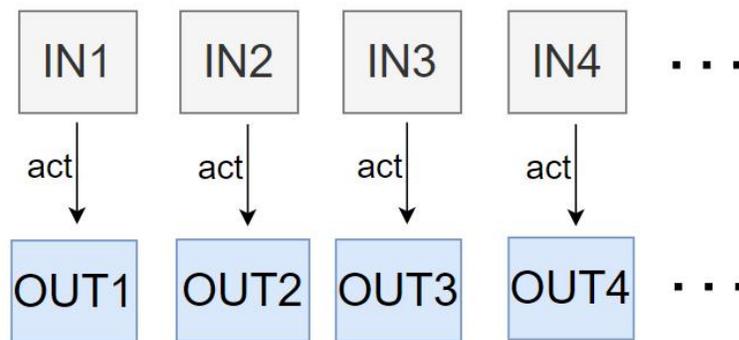
GELU激活函数的近似表示:

$$\text{GELU}(x) = 0.5x(1 + \tanh(\sqrt{2/\pi}(x + 0.044715x^3)))$$

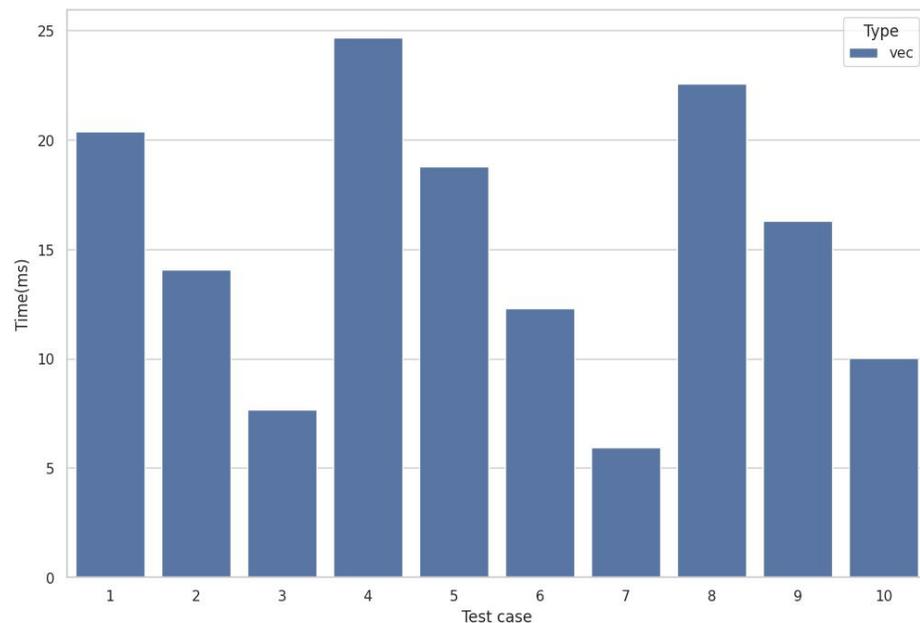


GELU激活函数及其导数

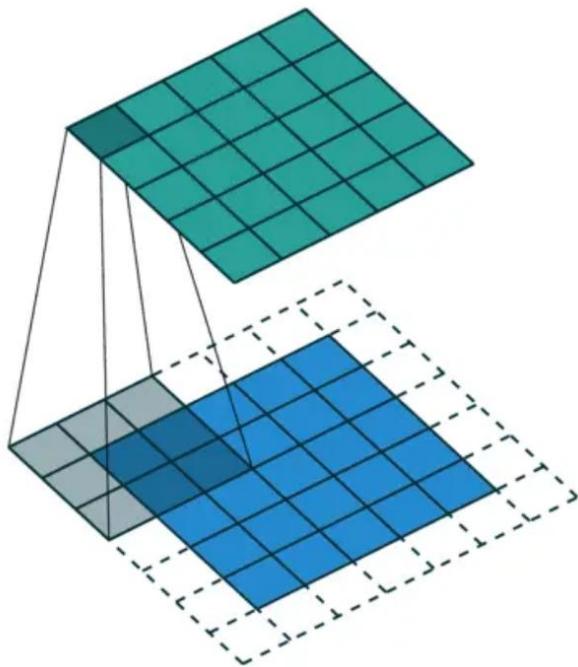
- 边界处理: 不足以打包成向量化处理的数据可以退化为标量处理
- 数学库: 使用提供好的数学库实现GELU激活函数(tanh和power)
- 常量向量的初始化: 由于需要调用提供的接口实现SIMD操作, 需要常量向量作为辅助计算



激活函数属于Elementwise操作



GELU赛题的用时



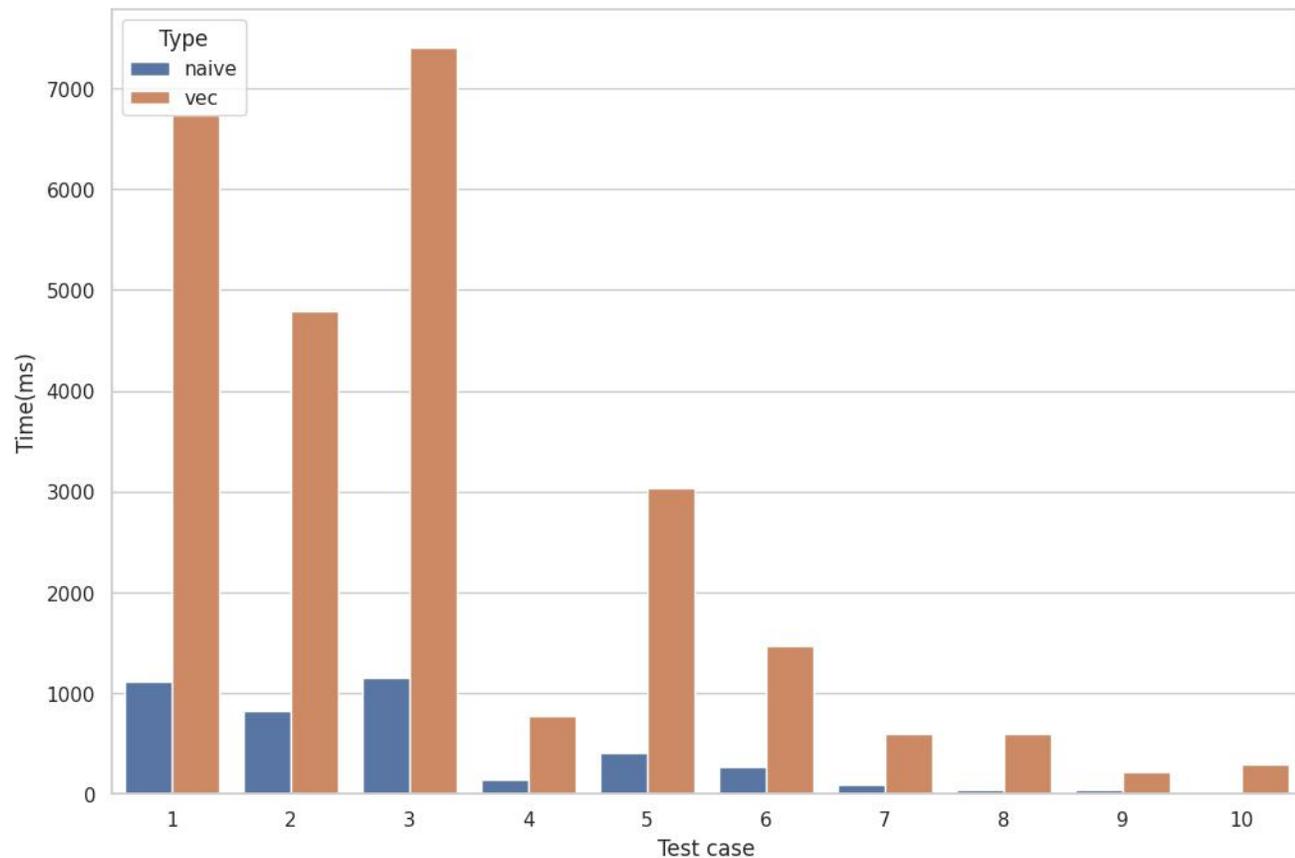
通用卷积算法图示

卷积操作的后端实现
(CUDNN、MKL等库)

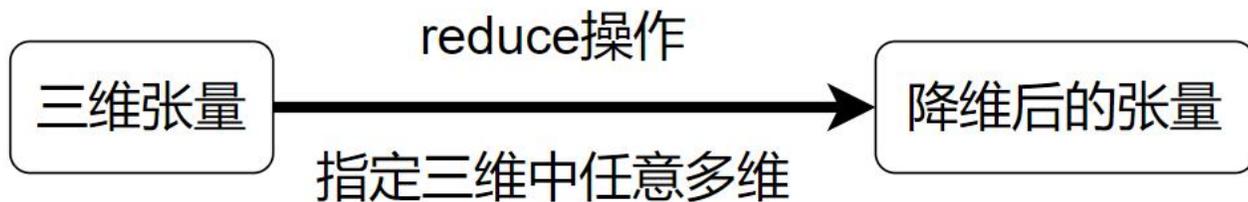
- FFT
- img2col
- Winograd

本次竞赛尝试的方法

- 直接卷积计算
- 向量化



直接卷积(naive)和向量化(vec)方法所用时间对比

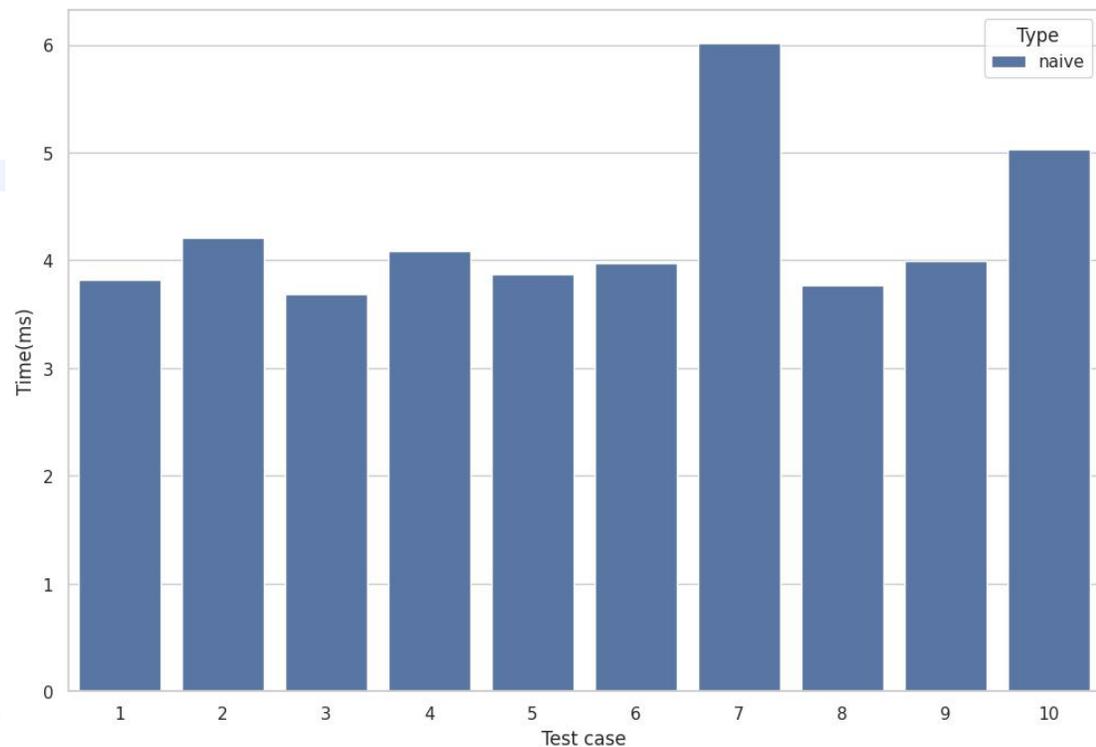


```

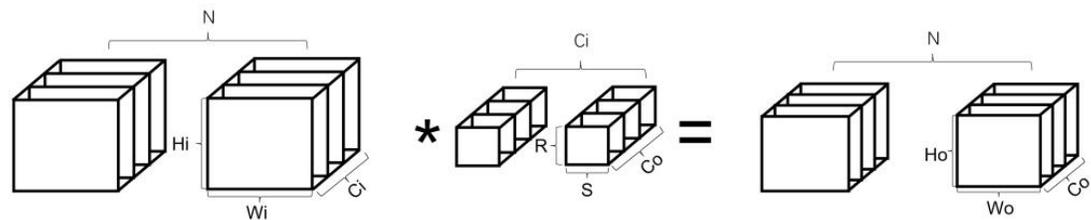
1  template<int config>
2  void REDUCE(float * __restrict input, float * __restrict output,
3             int dim0, int dim1, int dim2,
4             bool reduce_dim0, bool reduce_dim1, bool reduce_dim2) {
5      int out_dim2 = reduce_dim2 ? 1 : dim2;
6      int out_dim1 = reduce_dim1 ? 1 : dim1;
7      int out_dim0 = reduce_dim0 ? 1 : dim0;
8      int out_size = out_dim0 * out_dim1 * out_dim2;
9      for (int j = 0; j < dim1; j++) {
10         int jj = !reduce_dim1 * j;
11         for (int i = 0; i < dim0; i++) {
12             int ii = !reduce_dim0 * i;
13             for (int k = 0; k < dim2; k++) {
14                 int kk = !reduce_dim2 * k;
15                 int out_idx = ii * out_dim1 * out_dim2 + jj * out_dim2 + kk;
16                 int in_idx = i * dim1 * dim2 + j * dim2 + k;
17                 output[out_idx] =
18                     ii == i && jj == j && kk == k ? input[in_idx] : reduce_operation<config>(output[out_idx], input[in_idx]);
19             }
20         }
21     }
22 }

```

实现reduce操作的代码



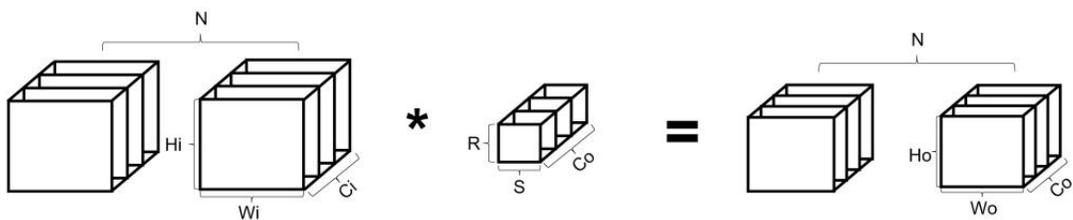
reduce赛题的用时



Hi: Input Height
Wi: Input Width
Ci: Input Channel
N: Batch size

R: Filter Height
S: Filter Width
Ci: Input Channel
Co: Filter Channel

Ho: Output Height
Wo: Output Width
Co: Output Channel
N: Batch size

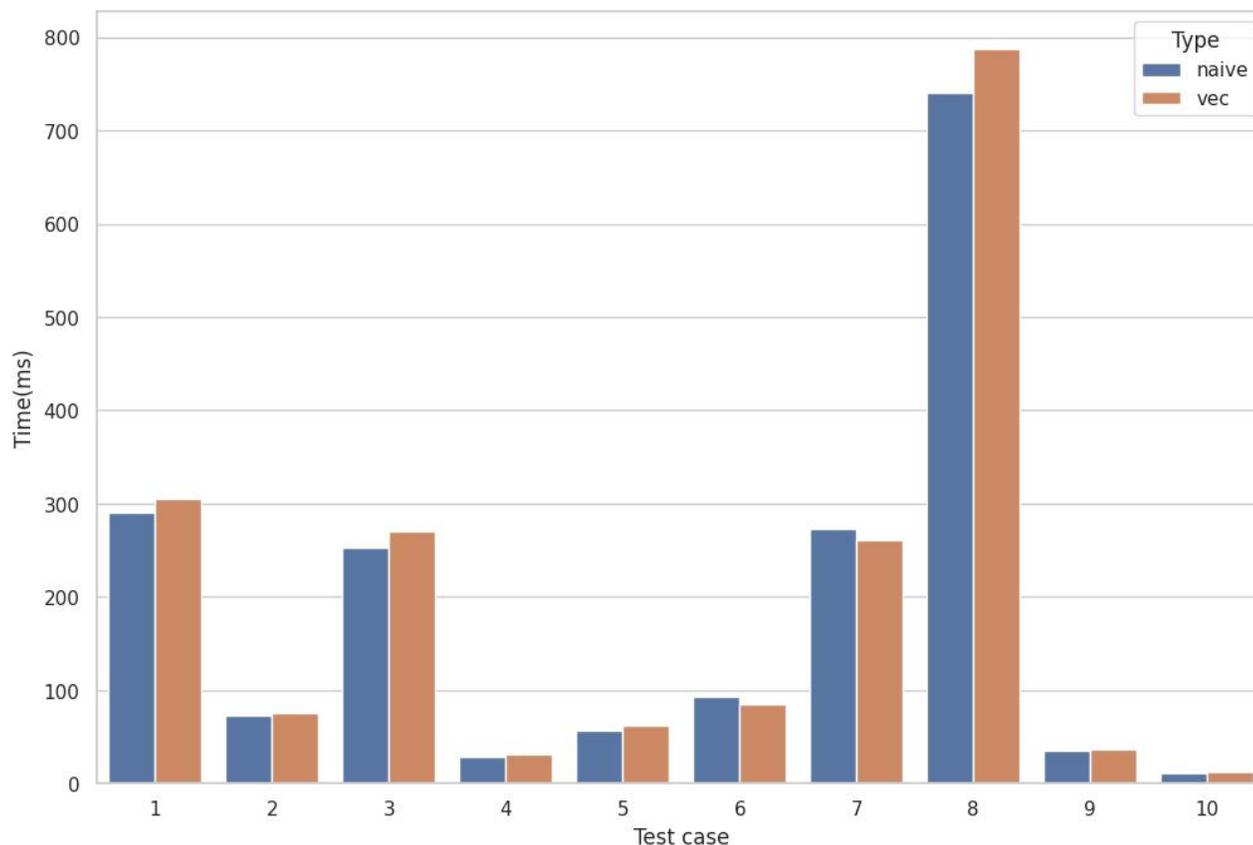


Hi: Input Height
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Ho: Output Height
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Co: Output Channel
N: Batch size

普通卷积(上)和Depthwise卷积(下)的对比



直接卷积(naive)和向量化(vec)方法所用时间对比

本次竞赛尝试的方法:

- 直接卷积计算
- 向量化

THANKS & QA

